

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10020034	FILING DATE 12/07/2001	CLASS 438	SUBCLASS 142	GAU 2812	EXAMINER L. LAM
**APPLICANTS: Mitros Jozef; Todd James; Pan Shanjen; Kubota Tsutomu;					
**CONTINUING DATA VERIFIED:					
**FOREIGN APPLICATIONS VERIFIED:					
EG-PUB/DO NOT PUBLISH <input type="checkbox"/>			RESCIND <input type="checkbox"/>		
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no			ATTORNEY DOCKET NO		
35 USC 119 condition met <input type="checkbox"/> yes <input type="checkbox"/> no			T-32931		
Verified and Acknowledged by Applicant's initials					
TITLE: Methods for reducing low CHC degradation MOSFET transistors					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner			
ISSUE FEE		Total Claims	
Amount Due	Date Paid	Print Claim for O.G.	
<input type="checkbox"/> TERMINAL DISCLAIMER		DRAWING	
		Sheets Drwg.	Figs. Drwg.
		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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